

## Electronic Acknowledgement Receipt

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<b>International Application Number:</b>	
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<b>Title of Invention:</b>	Test Circuit in Clock Synchronous Semiconductor Memory Device
<b>First Named Inventor/Applicant Name:</b>	5511029
<b>Customer Number:</b>	20277
<b>Filer:</b>	Harold C. McGurk
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<b>Attorney Docket Number:</b>	30693-0005RX9
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Power of Attorney	90012146_Power_Attorney_Reexam.pdf	1304116 1d599b566c5e500e50e482f197b56039cc829314	no	1

### Warnings:

### Information:

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2	Reexam Certificate of Service	90012146_Certificate_Service_5-7-12.pdf	89631 b5f1a4988f5f40806e576913a54303a03cda324f	no	2
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>				1393747	
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